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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: William J. Dally

Application No.: 09/761,172

Group: 2661

Filed: January 16, 2001

Examiner:

For: Multistage Digital Cross Connect With Integral Frame Timing

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CERTIFICATE OF MAILING	
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to Assistant Commissioner for Patents, Washington, D.C. 20231	
on <u>5-23-01</u>	<u>CLARE J. HANDALIAN</u>
Date	Signature
<u>CLARE J. HANDALIAN</u>	
Typed or printed name of person signing certificate	

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents

Washington, D.C. 20231

Sir:

Please amend the application as follows:

In the Specification

Please replace the paragraph at page 2, lines 21 through 29 with the following paragraph:

A digital cross connect can be implemented in a straightforward manner by demultiplexing each input port, switching all of the time slots of all of the input ports with a space switch, and then multiplexing each output port. This approach is illustrated in Figure 2.

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